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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/879,804	06/12/2001	Chiaki Imaeda	93198-000223	3726
27572	7590 12/16/2003		EXAMINER	
HARNESS, DICKEY & PIERCE, P.L.C. P.O. BOX 828			CHUNG, DAVID Y	
	D HILLS, MI 48303		ART UNIT	PAPER NUMBER
	, · · · · · · · · · · · · · ·		2871	

Please find below and/or attached an Office communication concerning this application or proceeding.

		App	olication No.	Applicant(s)			
			/879,804	IMAEDA, CHIAKI	iV		
Office Action Summary			ıminer	Art Unit	<u> </u>		
		Dav	rid Y. Chung	2871			
Period f	The MAILING DATE of this commเ or Reply	unication appears	on the cover sheet	with the correspondence addre)ss		
A SH THE - Exte afte - If th - If No - Faili - Any	HORTENED STATUTORY PERIOD MAILING DATE OF THIS COMMUI ensions of time may be available under the provisio re IV SIX (6) MONTHS from the mailing date of this core period for reply specified above is less than thirty O period for reply is specified above, the maximum ure to reply within the set or extended period for representation of the property received by the Office later than three monthined patent term adjustment. See 37 CFR 1.704(b).	NICATION. Ins of 37 CFR 1.136(a). Inmunication. (30) days, a reply within statutory period will appliply will, by statute, cause	In no event, however, may the statutory minimum of the ly and will expire SIX (6) MC the application to become.	a reply be timely filed nirty (30) days will be considered timely. DNTHS from the mailing date of this comma ABANDONED (35 U.S.C. § 133).	unication.		
1)⊠	Responsive to communication(s) f	iled on <u>10 Septer</u>	nber 2003.				
	This action is FINAL .	2b)⊠ This action					
3)	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposit	tion of Claims						
6)⊠ 7)⊠	4a) Of the above claim(s) 3,7,10,17 Claim(s) 41,44-46 and 56 is/are all Claim(s) 1,4,5,8,11-15,18,19,30-32 Claim(s) 2,6,9,16 and 48 is/are obj Claim(s) are subject to restr	lowed. 2 <u>,47,49,50 and 59</u> jected to.	9-62 is/are rejected		leration.		
	tion Papers		·····				
10) 11)	The specification is objected to by the drawing(s) filed on is/ard Applicant may not request that any objected Replacement drawing sheet(s) including the oath or declaration is objected.	e: a) accepted jection to the drawing the correction is	ng(s) be held in abeya required if the drawin	ance. See 37 CFR 1.85(a). g(s) is objected to. See 37 CFR			
=	under 35 U.S.C. §§ 119 and 120						
* \$ 13) \(\triangle \) \(\tr	Acknowledgment is made of a claim All b) Some * c) None of: 1. Certified copies of the priorit 2. Certified copies of the priorit 3. Copies of the certified copies application from the Internat See the attached detailed Office act Acknowledgment is made of a claim since a specific reference was includ 7 CFR 1.78. a) The translation of the foreign la Acknowledgment is made of a claim eference was included in the first se	y documents hav y documents hav s of the priority do ional Bureau (PC ion for a list of the for domestic prio led in the first sen anguage provision for domestic prio	e been received. e been received in ocuments have been T Rule 17.2(a)). e certified copies notity under 35 U.S.Cottence of the specifical application has ority under 35 U.S.Cottence 35 U.S.C	Application No n received in this National Stant received. S. § 119(e) (to a provisional application or in an Application Databeen received. S. §§ 120 and/or 121 since a second control or since	oplication) ta Sheet.		
Attachmen	nt(s)						
1) Notice 2) Notice	ce of References Cited (PTO-892) ce of Draftsperson's Patent Drawing Review mation Disclosure Statement(s) (PTO-1449)			Summary (PTO-413) Paper No(s) Informal Patent Application (PTO-15			

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DETAILED ACTION

Election/Restrictions

Applicant's election with traverse of Group II in the response to restriction requirement filed on September 10, 2003 is acknowledged. The traversal is on the ground(s) that an undue burden is not placed on the examiner. This is not found persuasive because of the different classification of the subcombinations identified by the examiner.

The requirement is still deemed proper and is therefore made FINAL.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

1. Claims 1, 15, 19, 30, 31, 47, 49, 50, and 61 rejected under 35

U.S.C. 102(e) as being anticipated by Yoshii et al. (U.S. 6,147,724).

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As to claims 1, 30, 47, 49 and 50, Yoshii discloses a liquid crystal display module in figure 4A comprising a liquid crystal display panel and a backlighting unit. Note the panel substrate SUB1, drain driver chip IC1, and the drain spacer DSPC which covers the drain driver chip and has a notch structure NOT at a location corresponding to the drain driver chip. The drain spacer DSPC functions as a holding member and the notch structure NOT functions as a storing portion. See column 12, lines 51-58. The notch structure contains a recess for storing the drain driver chip IC1.

As to claims 15, 19 and 31, the drain spacer DSPC abuts against the panel substrate SUB1 and the notch structure functions as the storing portion for storing the drain driver chip IC1.

As to claim 61, the drain driver chip IC1 and gate driver chip IC2 are the control means for controlling the liquid crystal device.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

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2. Claims 4, 5, 8, 11-14, 18, 32, 59, 60 and 62 rejected under 35 U.S.C. 103(a) as being unpatentable over Yoshii et al. (U.S. 6,147,724).

As to claims 4, 5, 32, and 59, Yoshii et al. does not disclose a protective material formed on the surface of panel substrate SUB1. However, it was well known to cover the panel substrate with a protective material in order to protect the gate and drain driver chips from mechanical shock and electrical failure. Therefore, it would have been obvious to one of ordinary skill in the art at the time of invention to cover the panel substrate with a protective material in order to protect the driving chips from electrical failure and mechanical shock.

As to claims 8 and 11-14, Yoshii et al. does not disclose a plurality of drain driver chips mounted to the panel substrate and held by a notch structure. However, it was well known to provide multiple drain driver chips because this was a much more feasible way of driving a large display. Therefore, it would have been obvious to one of ordinary skill in the art at the time of invention to provide multiple drain driver chips because it was a more feasible way of driving a large display. It would have been obvious to provide corresponding notch structures in order to individually protect each drain driver chip.

As to claim 18, Yoshii et al. does not disclose an elastic holding portion for holding the panel substrate in a state abutted against the abutting portion.

However, it was well known to do this in order to reduce the amount of

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mechanical stress on the panel substrate. Therefore, it would have been obvious to one of ordinary skill in the art at the time of invention to provide an elastic holding portion in order to reduce the amount of mechanical stress on the panel substrate.

As to claim 60, the liquid crystal display panel shown in figure 4A is manufactured by disposing liquid crystal material between substrates SUB1 and SUB2.

As to claim 62, the drain driver chip IC1 and gate driver chip IC2 are the control means for controlling the liquid crystal device.

Allowable Subject Matter

Claims 41, 44-46 and 56 allowed.

Claims 2, 6, 9, 16 and 48 objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The following is a statement of reasons for the indication of allowable subject matter: the prior art did not teach or suggest a holding member disposed along an electro-optical panel so as to cover the electro-optical panel and having a storing portion for storing an electronic part mounted to the electro-optical panel, wherein the holding member is configured to function as a light guide.

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Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to David Chung whose telephone number is (703) 306-0155. The examiner can normally be reached on Monday-Friday from 8:30 am to 5:00 pm.

KENNETH PARKER PRIMARY EXAMINER